

FIG. 1

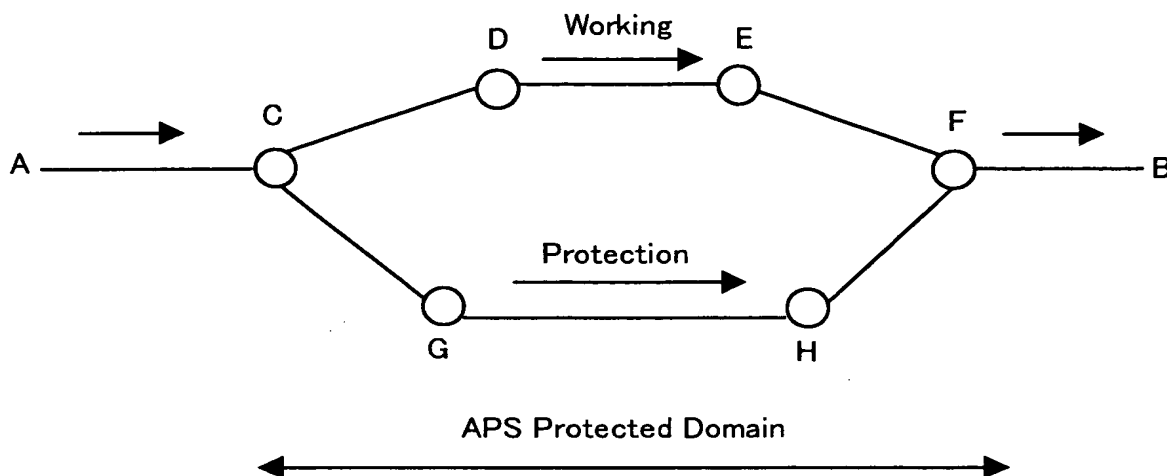
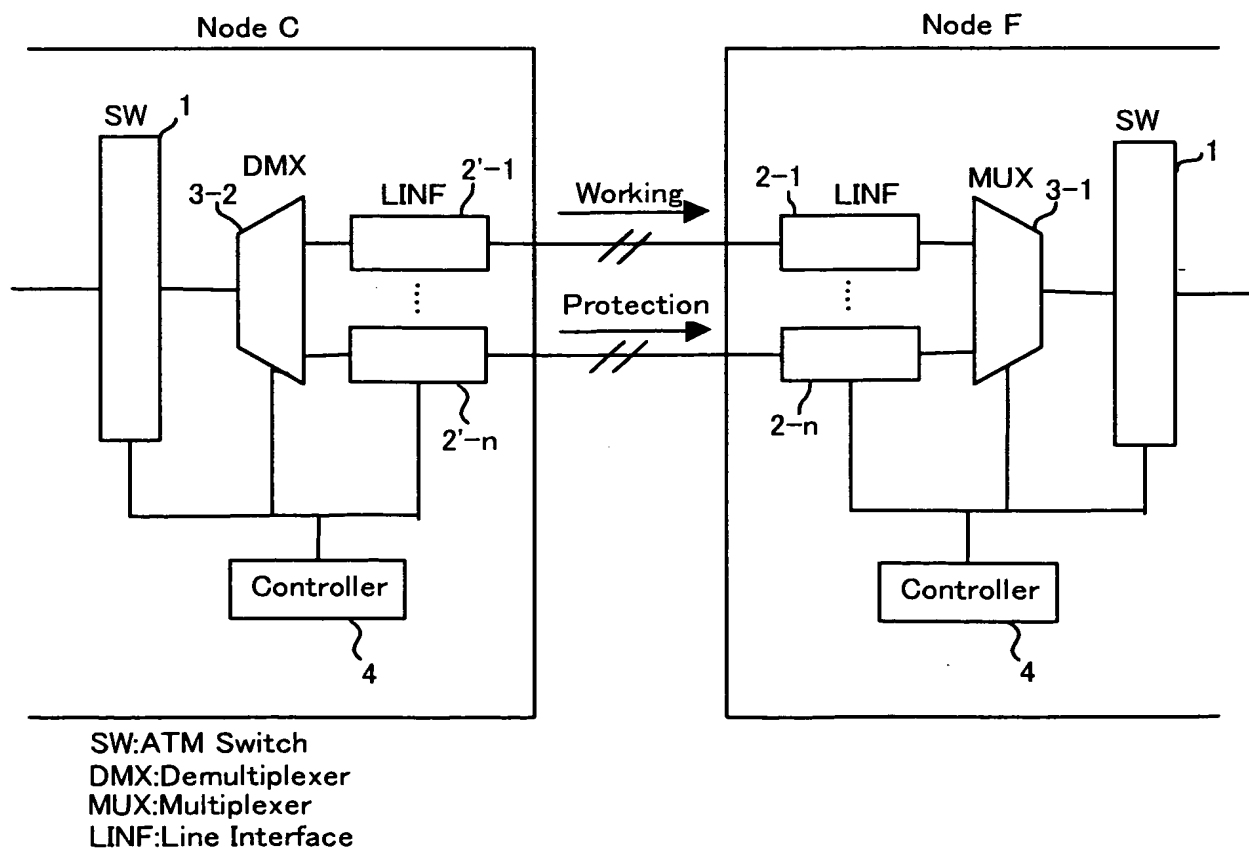
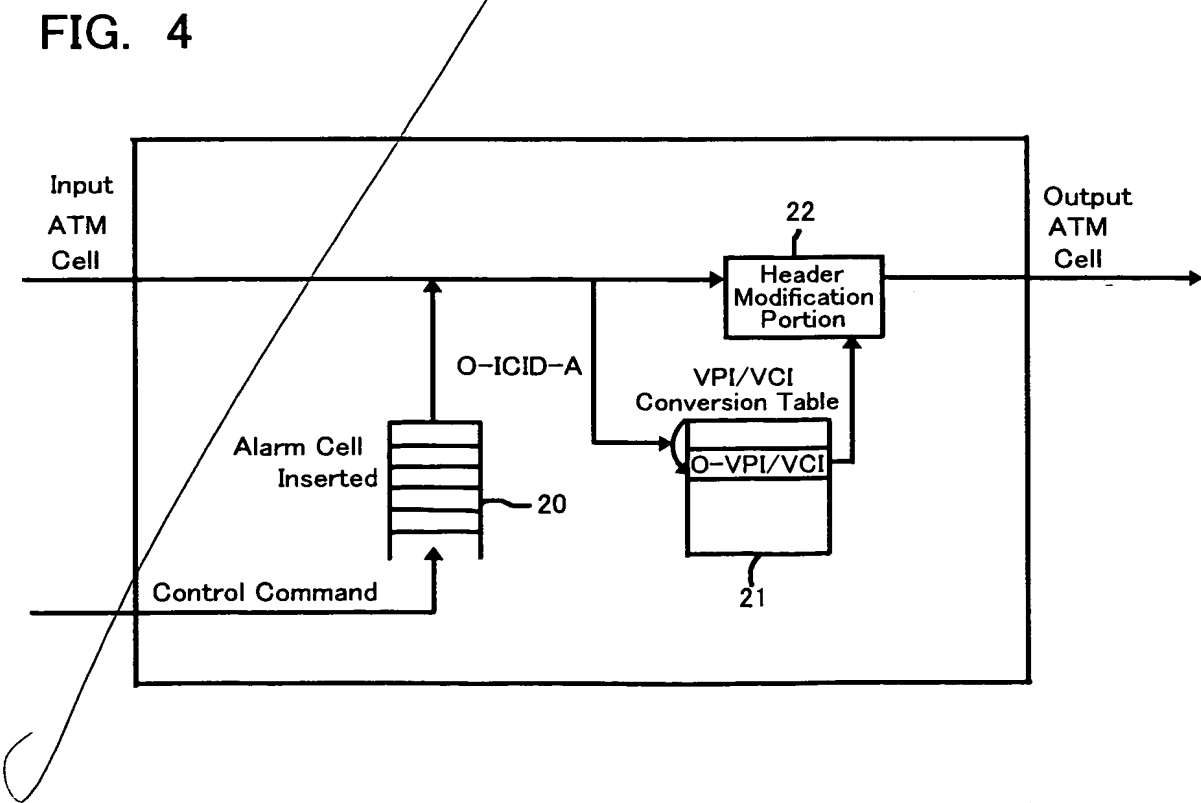


FIG. 2



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The diagram illustrates the O-ICID-A system architecture. It shows an input path for an "Input ATM Cell" and an output path for an "Output ATM Cell". A "Control Command" is input to a stack of six horizontal bars, labeled "Alarm Cell Inserted" and "20". An arrow labeled "O-ICID-A" points from this stack to a "Header Modification Portion" (labeled "22"). Another arrow labeled "O-ICID-A" points from the input path to a "VPI/VCI Conversion Table" (labeled "21"). Inside this table is a section labeled "O-VPI/VCI". An arrow points from the "O-VPI/VCI" section to the "Header Modification Portion". The entire system is enclosed in a rectangular frame with a diagonal line crossing from the bottom-left to the top-right.

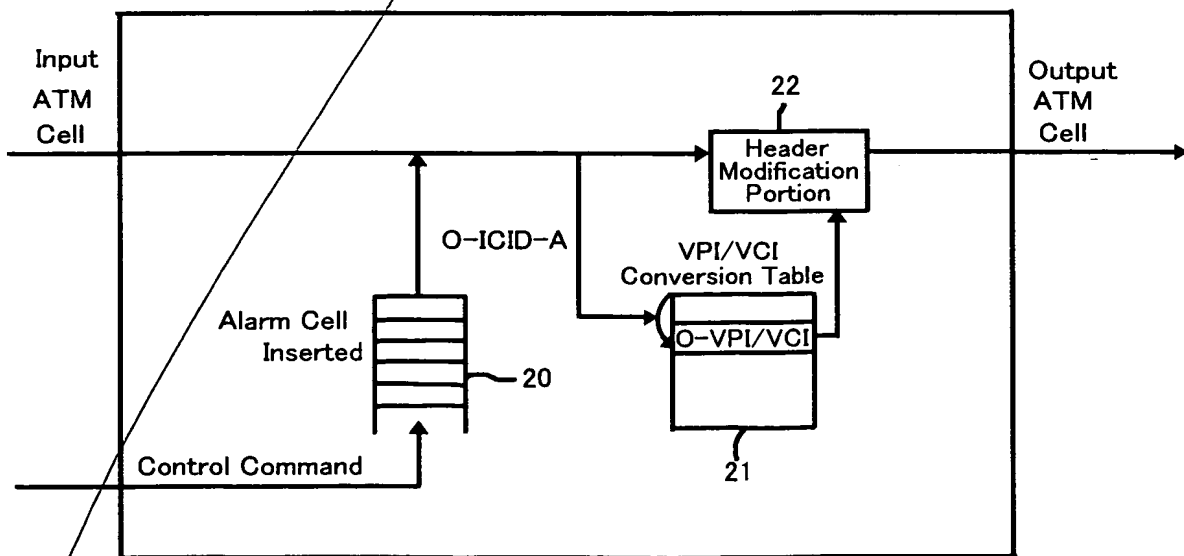


FIG. 5

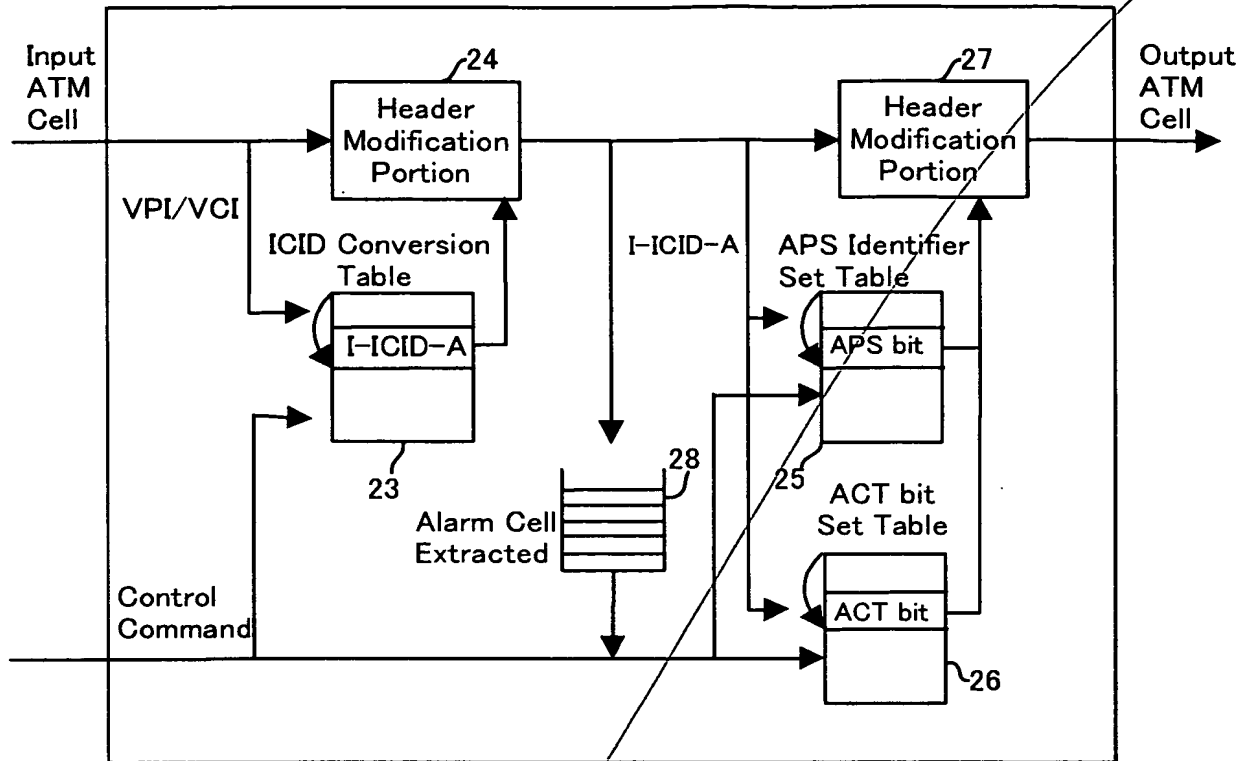
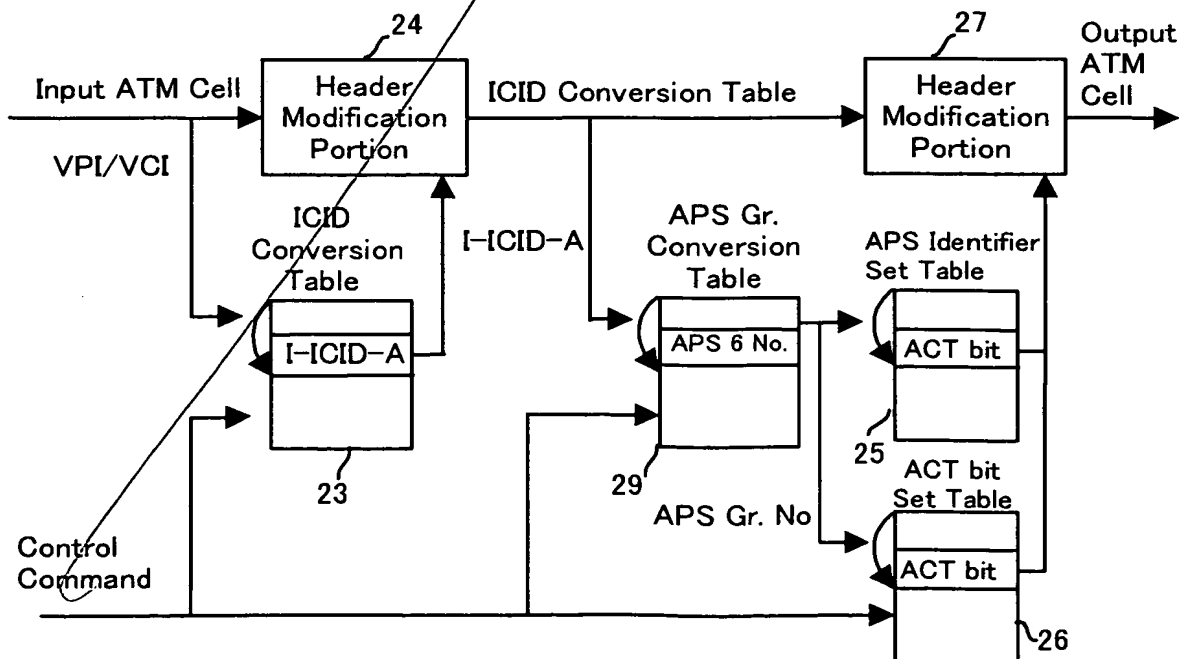


FIG. 6



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